



| L Number | Hits | Search Text | DB | Time stamp |
|----------|---------|---------------------------------------------------------------------------------------------------------|-----------------------|------------------|
| 1 | 11 | 704/275.ccls. and processors and pda and dsp and (speech | USPAT; | 2003/09/24 20:35 |
| _ | | adj1 recognition) and (command instruction) and pda | US-PGPUB | |
| 2 | 1 | 704/275.ccls. and (CPU (maim adj1 processor)) and dsp and | USPAT; | 2003/09/24 20:38 |
| | | (command) and pda and (speech adj1 recognition) | US-PGPUB | 0000/04/47 04 00 |
| - | 19 | (motherboard and computer and (cpu microprocessor | USPAT; | 2003/04/17 21:20 |
| | | processor) and dsp and memory) and ((spoken voice | US-PGPUB; | |
| | | speech) adj1 command) | EPO; JPO; DERWENT; | |
| | | | IBM TDB | |
| _ | 33 | (motherboard and computer and (cpu microprocessor | USPAT; | 2003/04/17 21:20 |
| | | processor) and dsp and memory) and ((spoken voice speech | US-PGPUB | |
| | | audio) adj1 command) | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| - | 16 | (motherboard and computer and (cpu microprocessor | USPAT; | 2003/04/17 21:21 |
| | | processor) and dsp and memory) and ((spoken voice speech | US-PGPUB; | |
| | | audio) adj1 command) and pci | EPO; JPO; | |
| | | | DERWENT; IBM_TDB | |
| _ | 13 | (motherboard and computer and (cpu microprocessor | USPAT: | 2003/04/17 21:24 |
| - | , , , , | processor) and dsp and memory) and ((spoken voice speech | US-PGPUB; | 2000/04/17 21.24 |
| | | audio) adj1 command) and pci and bus and power and input | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| - | 3 | (motherboard and computer and (cpu microprocessor | USPAT; | 2003/04/17 21:24 |
| | | processor) and dsp and memory) and ((spoken voice | US-PGPUB; | |
| | | speech) adj1 command) and pci and bus and power and | EPO; JPO; | |
| | | input | DERWENT; | |
| | 38 | (motherboard PCB) and computer and (cpu microprocessor | IBM_TDB USPAT; | 2003/04/17 21:56 |
| 1 | , 00 | processor) and dsp and memory and ((spoken speech verbal | US-PGPUB; | 2000/04/17 21:00 |
| 1 | | voice audio) adj1 command) | EPO; JPO; | |
| | | , | DERWENT; | |
| | | | IBM_TDB | |
| - | 11 | ((motherboard PCB) and computer and (cpu microprocessor | USPAT; | 2003/04/17 22:00 |
| | , | processor) and dsp and memory and ((spoken speech verbal | US-PGPUB; | |
| | | voice audio) adj1 command)) and 704/\$.ccls. | EPO; JPO; | |
| | 1 | | DERWENT; IBM TDB | |
| _ | 10 | ((motherboard PCB) and computer and (cpu microprocessor | USPAT; | 2003/04/17 21:54 |
| | | processor) and dsp and memory and ((spoken speech verbal | US-PGPUB; | |
| | | voice audio) adj1 command)) and 704/\$.ccls. and bus | EPO; JPO; | |
| | | , , , | DERWENT; | |
| | | | IBM_TDB | |
| - | 13 | ((motherboard PCB) and computer and (cpu microprocessor | USPAT; | 2003/04/17 21:52 |
| | | processor) and dsp and memory and ((spoken speech verbal | US-PGPUB; | |
| | | voice audio) adj1 command)) and pci and power | EPO; JPO; DERWENT; | |
| | | | IBM_TDB | |
| · _ | 47 | (motherboard PCB (main adj1 circuit adj1 board) backplane) | USPAT: | 2003/04/17 21:58 |
| | | and computer and (cpu microprocessor processor) and dsp | US-PGPUB; | |
| | | and memory and ((spoken speech verbal voice audio) adj1 | EPO; JPO; | |
| | | command) | DERWENT; | |
| | | | IBM_TDB | |
| - | 50 | ((motherboard mother-board (mother board) PCB (main adj1 | USPAT; | 2003/04/18 16:02 |
| | | circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | |
| | | microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and | EPO; JPO; DERWENT; | |
| | | ((spoken speech verbal voice audio) adji command)) and 704/\$.ccls. | IBM_TDB | |
| _ | 1 | ((motherboard mother-board (mother board) PCB (main adj1 | USPAT; | 2003/04/17 22:02 |
| | • | circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| | | ((spoken speech verbal voice audio) adj1 command)) and | DERWENT; | |
| | | 704/\$.ccls. and pci | IBM_TDB | |



| - | 24 | ((motherboard mother-board (mother board) PCB (main adj1 | USPAT; | 2003/04/17 22:10 |
|---|------|-------------------------------------------------------------------------------------------------------------------|------------------------|--------------------|
| | | circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| | | ((spoken speech verbal voice audio) adj1 command)) and pci | DERWENT; | |
| | | and (memory same dsp) | IBM_TDB | |
| - | 20 | ((motherboard mother-board (mother board) PCB (main adj1 | USPAT; | 2003/04/18 20:54 |
| | | circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| • | | ((spoken speech verbal voice audio) adj1 command)) and pci | DERWENT; | |
| | 32 | and (memory same dsp) and power (((motherboard mother-board (mother board) PCB (main | IBM_TDB USPAT; | 2003/04/17 22:11 |
| _ | 32 | adj1 circuit adj1 board) backplane) and computer and (cpu | US-PGPUB: | 2003/04/1/ 22.11 |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| | | ((spoken speech verbal voice audio) adj1 command)) and | DERWENT; | |
| | | 704/\$.ccls.) and (memory same dsp) | IBM TDB | |
| _ | 23 | (((motherboard mother-board (mother board) PCB (main | USPAT; | 2003/04/17 22:19 |
| | | adj1 circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | 2000/0 11 11 22:10 |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| | 1 | ((spoken speech verbal voice audio) adj1 command)) and | DERWENT; | |
| | | 704/\$.ccls.) and bus and (memory same dsp) and power | IBM_TDB | |
| - | 1 | "05844824" | USPAT; | 2003/04/17 22:19 |
| | | · | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| - | 24 | (((motherboard mother-board (mother board) PCB (main | USPAT; | 2003/04/17 22:21 |
| | | adj1 circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| | | ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and bus and (memory same dsp) | DERWENT; | |
| _ | 50 | ((motherboard mother-board (mother board) PCB (main adj1 | IBM_TDB USPAT; | 2003/04/18 10:41 |
| _ |] 30 | circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | 2003/04/16 10.41 |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| | | ((spoken speech verbal voice audio) adj1 command) and | DERWENT; | |
| | | (bus pci)) and pci | IBM_TDB | |
| _ | 34 | ((motherboard mother-board (mother board) PCB (main adj1 | USPAT; | 2003/04/18 10:42 |
| | | circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| | | ((spoken speech verbal voice audio) adj1 command) and | DERWENT; | |
| | | (bus pci)) and pci and speech | IBM_TDB | |
| - | 29 | ((motherboard mother-board (mother board) PCB (main adj1 | USPAT; | 2003/04/18 16:06 |
| | | circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| | | ((spoken speech verbal voice audio) adj1 command) and | DERWENT; | |
| |] | (bus pci) and pci and (speech adj1 recognit\$4) | IBM_TDB | 2002/04/49 45:57 |
| - | 20 | ((motherboard mother-board (mother board) PCB (main adj1 | USPAT; | 2003/04/18 15:57 |
| | | circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and | US-PGPUB; EPO; JPO; | |
| | | ((spoken speech verbal voice audio) adj1 command) and | DERWENT: | |
| | | (spoken speech verbal voice addits) adj r command) and (bus pci)) and pci and (speech adj1 recognit\$4) and power | IBM_TDB | |
| _ | 19 | (((motherboard mother-board (mother board) PCB (main | USPAT; | 2003/04/18 16:05 |
| | " | adj1 circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | 2300/0 // 10 10.00 |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| | | ((spoken speech verbal voice audio) adj1 command) and | DERWENT: | |
| | | (bus pci)) and pci and (speech adj1 recognit\$4) and power) | IBM_TDB | |
| | | and dynamic\$5 and command | _ | |
| - | 1 | (((motherboard mother-board (mother board) PCB (main | USPAT; | 2003/04/18 16:00 |
| | | adj1 circuit adj1 board) backplane) and computer and (cpu | US-PGPUB; | |
| | | microprocessor processor) and dsp and memory and | EPO; JPO; | |
| | | ((spoken speech verbal voice audio) adj1 command) and | DERWENT; | |
| | | (bus pci)) and pci and (speech adj1 recognit\$4) and power) | IBM_TDB | |
| | L | and dynamic\$5 and command and phoneme | | |

| • | 50 | ((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and | USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2003/04/18 16:15 |
|---|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------|------------------|
| - | 42 | 704/\$.ccls. (((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/04/18 16:17 |
| - | 29 | ((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command) and (bus pci)) and pci and (speech adj1 recogni\$4) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/04/18 16:15 |
| - | 32 | (((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/04/18 16:18 |
| - | 11 | (((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme and (continuous adj1 speech) and (voice adj1 command) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/04/18 18:19 |
| - | 20 | (((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/04/18 20:50 |
| - | 6 | "06154830" "06085314" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/04/18 18:24 |
| - | 2 | "06170049" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2003/04/18 18:24 |
| - | 20 | (((motherboard mother-board (mother board) PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/04/18 20:53 |
| - | 10 | (((motherboard mother-board)) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio)) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice)) adj1 recognition) and bus and phoneme | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/04/18 20:51 |
| - | 20 | ((((mother board) PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/04/18 20:53 |
| - | 11 | ((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and (memory same dsp) and power | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/04/18 20:55 |



| - | 87 | PCI and PDA and dsp and cpu | USPAT; US-PGPUB | 2003/04/18 21:28 |
|---|----------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------|------------------|
| - | 36 | PCI and PDA and dsp and cpu and ((speech voice) adj1 recognition) | USPAT; US-PGPUB | 2003/04/18 22:42 |
| - | 9 | PCI and ((cellular mobile) adj1 phone) and (speech adj1 recognition) and dsp | USPAT; US-PGPUB | 2003/04/18 22:55 |
| - | 3 | PCI and ((cellular mobile) adj1 phone) and vocabulary | USPAT; | 2003/04/18 22:56 |
| - | 46 | 704/\$.ccls. and ((mobile (phone telephone)) or (cellular phone)) and DSP and (noise adj1 cancel\$6) | US-PGPUB USPAT; US-PGPUB; | 2003/04/19 20:11 |
| | | phonoly and bor and (noise day) canonger | EPO; JPO; DERWENT; IBM TDB | |
| - | 4 | (dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing) | USPAT; US-PGPUB | 2003/04/19 21:03 |
| - | 7 | (dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing) | USPAT; US-PGPUB; EPO; JPO; | 2003/04/19 21:05 |
| - | 0 | (menu near1 select\$) and (voice adj1 dialing) and (noise adj1 cancellation) | DERWENT; IBM_TDB USPAT; US-PGPUB; | 2003/04/19 21:29 |
| | | cancellation) | EPO; JPO; DERWENT; IBM_TDB | |
| - | 5 | (voice adj1 dialing) and (dsp (dsp adj1 chip)) and (menu adj1 select\$) | USPAT; US-PGPUB; EPO; JPO; | 2003/04/19 22:54 |
| - | 5 | (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu adj1 select\$) | DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; | 2003/04/19 23:05 |
| - | 13 | (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu) | DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; | 2003/04/19 23:10 |
| - | 20 | (noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition) | DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2003/04/19 23:14 |
| - | 5 | ((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) and (dsp same (speech adj1 recognition)) | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2003/04/19 23:13 |
| - | 1 | (noise adj1 cancel\$5) same (dsp) and (speech adj1 recognition) | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2003/04/19 23:14 |
| _ | 2 | 6182203.URPN. | IBM_TDB USPAT | 2003/04/26 13:26 |
| - | 0 | 6539467.URPN. | USPAT | 2003/04/26 13:28 |
| - | 0 | 6453405.URPN. | USPAT | 2003/04/26 13:28 |
| - | 17 | ("4814976" "5072418" "5329471" "5535331" "5617543" "5655098" "5752273" "5864703" "6073228" "6167466" "6182203" "6209082" "6219773" "6260137" "6266686" "6289418" "6349383").PN. | USPAT | 2003/04/26 13:28 |
| - | 0 | 704/\$.ccls. and CPU and dsp and (command adj1 mode) and pda | USPAT; US-PGPUB | 2003/09/22 23:09 |
| | <u> </u> | | | |





| - | 8 | 704/\$.ccls. and CPU and dsp and (command) and pda | USPAT; US-PGPUB | 2003/09/22 22:51 |
|----------------|-----|------------------------------------------------------------------------------------------------|--------------------|------------------|
| - | 5 | 704/\$.ccls. and CPU and dsp and (command and mode) and | USPAT; US-PGPUB | 2003/09/22 23:10 |
| - | 5 | pda 704/\$.ccls. and (CPU (maim adj1 processor)) and dsp and | USPAT; US-PGPUB | 2003/09/22 23:12 |
| - | 1 | (command and mode) and pda 704/231.ccls. and (CPU (maim adj1 processor)) and dsp and | USPAT; | 2003/09/22 23:13 |
| - | 13 | (command) and pda 704/\$.ccls. and (CPU (maim adj1 processor)) and dsp and | US-PGPUB USPAT; | 2003/09/24 20:37 |
| - | 50 | (command) and pda and (speech adj1 recognition) 704/\$.ccls. and motorola and dsp and (command | US-PGPUB USPAT; | 2003/09/23 10:15 |
| _ | 1 | instruction) and (speech adj1 recognition) 5873062.URPN. | US-PGPUB USPAT | 2003/09/23 09:59 |
| - | 1 1 | "5640490".PN. | USPAT | 2003/09/23 10:00 |
| 1_ | 31 | 5640490.URPN. | USPAT | 2003/09/23 10:00 |
| ļ ⁻ | 25 | ("3581192" "3703609" "3838217" "3938394" "3940565" | USPAT | 2003/09/23 10:08 |
| - | 23 | | USFAI | 2003/09/23 10:00 |
| |) | "3969972" "4181813" "4452079" "4658252" "4780906" | | |
| | • | "4817154" "4852170" "4862503" "4975957" "4991216" | | |
| | | "4998280" "5027410" "5065432" "5068900" "5091948" | | |
| | | "5121434" "5166981" "5202926" "5299125" "5321608").PN. | | |
| 1_ | 1 | 704/\$.ccls. and dedicat\$ near (speech adj1 processor) | USPAT; | 2003/09/23 11:02 |
| - | · ' | 70474.ccis. and dedicate hear (speech adj) processory | US-PGPUB | 2003/09/23 11.02 |
| _ | 1 | 6230138.URPN. | USPAT | 2003/09/23 10:18 |
| _ | 1 | 6230138.URPN. | USPAT | 2003/09/23 10:18 |
| _ | 42 | 704/\$.ccls. and (two adj1 processors) | USPAT; | 2003/09/23 11:03 |
| | '- | , , , , , , , , , , , , , , , , , , , | US-PGPUB | |
| 1_ | 21 | 704/\$.ccls. and (two adj1 processors) and dsp | USPAT; | 2003/09/23 11:36 |
| - | ' | 1 70474.ccis. and (two adj i processors) and dsp | US-PGPUB | 2003/03/23 11.30 |
| | 4 | 704/C sale and (hive adid pressure) and den and ade | | 2002/00/02 44:04 |
| - | 1 | 704/\$.ccls. and (two adj1 processors) and dsp and pda | USPAT; US-PGPUB | 2003/09/23 11:04 |
| | 2 | 704/C sele and (hus adid pressure) and ade | | 2002/00/22 44:04 |
| - | 2 | 704/\$.ccls. and (two adj1 processors) and pda | USPAT; | 2003/09/23 11:04 |
| | | 704/0 (4 | US-PGPUB | 0000/00/00 44:05 |
| - | 3 | 704/\$.ccls. and (two adj1 processors) and pda | USPAT; | 2003/09/23 11:05 |
| | | | US-PGPUB | |
| - | 2 | 704/\$.ccls. and (two adj1 processors) and pda | USPAT; | 2003/09/23 11:36 |
| | | | US-PGPUB | |
| - | 284 | 704/\$.ccls. and processors and pda | USPAT; | 2003/09/23 11:37 |
| | | | US-PGPUB | |
| - | 63 | 704/\$.ccls. and processors and pda and dsp | USPAT; | 2003/09/23 11:37 |
| | | | US-PGPUB | |
| - | 43 | 704/\$.ccls. and processors and pda and dsp and (speech | USPAT; | 2003/09/24 20:35 |
| | | adj1 recognition) and (command instruction) and pda | US-PGPUB | |
| _ | 23 | 704/\$.ccls. and processors and pda and dsp and (speech | USPAT; | 2003/09/23 11:54 |
| _ | 25 | adj1 recognition) and (command instruction) and bus | US-PGPUB | 2000/00/20 11:04 |
| | 3 | 704/\$.ccls. and processors and pda and dsp and (speech | USPAT; | 2003/09/23 11:56 |
| - | 1 3 | | | 2003/08/23 11.30 |
| | | adj1 recognition) and (command instruction) and bus and | US-PGPUB | |
| | 70 | cpu | LICDAT | 0000/00/04 40:45 |
| - | 70 | processors and pda and dsp and (speech adj1 recognition) | USPAT; | 2003/09/24 18:15 |
| | _ | and (command instruction) and bus and cpu | US-PGPUB | |
| - | 5 | processors and pda and dsp and (speech adj1 recognition) | USPAT; | 2003/09/23 12:00 |
| 1 | | and (command instruction) and bus and cpu and (reduce | US-PGPUB | |
| 1 | | adj2 (power consumption)) | | |
| - | 6 | processors and pda and dsp and (speech adj1 recognition) | USPAT; | 2003/09/24 18:18 |
| i | | and (command instruction) and bus and cpu and ((power | US-PGPUB | |
| 1 | | adj1 saving) (reduc\$ adj1 power)) | | |
| | ı | , · · · · · · · · · · · · · · · · | L | L |